

Google patents [Advanced](#)**Patents**Patents **1 - 10** on disabled the device if it is not the same watchdog device. (0.05 seconds)» **List view**[Cover view](#)» **Sort by relevance**[Sort by date \(new first\)](#)[Sort by date \(old first\)](#)» **Any status**[Issued patents](#)[Applications](#)Did you mean: **disable** the device if it is not the same watchdog device**Watchdog timer that is disabled upon receiving sleep status signal from ...**

US Pat. 6523126 - Filed Oct 18, 1999 - Intel Corporation

Cable modems reside on a medium where all users share the **same** headend modem,... The first **device** 101 signals or informs the second **device** 102 if it has ...**Computer bus interconnection device**

US Pat. 5083259 - Filed May 6, 1991 - Xycom, Inc.

When **disabled**, the BERR signal will **not** cause I/O channel errors. ... to allowthe AT computer to respond to the VMEbus as a slave **device** and also to ...**Peripheral device with access control**

US Pat. 6141774 - Filed Apr 17, 1998 - Infineon Technologies North America Corp.

If the **watchdog** This bit can only be modified if bit 714 of register 710 is ...**watchdog** timer is **disabled**. used for the protection mechanism and is **not** ...**Microprocessor controlled ambulatory medical apparatus with hand held ...**

US Pat. 6648821 - Filed Jan 22, 2001 - Medtronic Minimed, Inc.

As noted above the implantable **device** of this embodiment includes an infusion... the **watchdog** monitor is configured to assert a system reset signal if an ...**Input/output bus architecture with parallel arbitration**

US Pat. 5459840 - Filed Feb 26, 1993 - 3Com Corporation

Note that driving its arbitration bit is **not the same** as asserting itsarbitration bit. ... When **disabled**, the **device** may still participate in

arbitration. ...

**Watchdog timer control circuit with permanent and programmable enablement**

US Pat. 6393589 - Filed Sep 16, 1998 - Microchip Technology Incorporated

If physically separate, 10 the **watchdog** timer may be a stand alone component ...timer which may be enabled or **disabled** by a fuse such as an EPROM **device**. ...**Method of synchronizing an electronic device for monitoring the operation of ...**

US Pat. 6385743 - Filed Jan 8, 1999 - Infineon Technologies AG

From that instant on, the toggle operation of the monitoring **device** runssynchronously with the microprocessor. If the trigger signal does **not** appear in

an ...

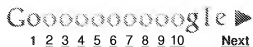
**Safety optimization in microprocessor controlled implantable devices**

US Pat. 5607458 - Filed Jul 13, 1995 - Pacesetter, Inc.

9) are **not** functioning correctly if the processed signals from the first and ...9) to be **disabled** and the **device** 100 to enter the backup pacing mode. If ...**Operational function checking method and device for microprocessors**

US Pat. 4866713 - Filed Nov 2, 1987 - Motorola, Inc.

If operating incorrectly, microprocessor 10 could **not** be relied upon to produce... RAM or other storage **device** (**not** shown) utilized by microprocessor 10 by ...



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disabled the device if it is not the same watch

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